

Notice of References Cited		Application/Control No.	Applicant(s)/Patent Under Reexamination	
		10/764,515	LEE, CHUN-LIANG	
		Examiner	Art Unit	
		David E. Martinez	2181	Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	A	US-2002/0026532	02-2002	Maeda et al.	709/250
	B	US-			
	C	US-			
	D	US-			
	E	US-			
	F	US-			
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FOREIGN PATENT DOCUMENTS

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	N					
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NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Serial and Parallel cable schematics and wiring diagrams, http://www.jspayne.com/io/schematics.html , Copyright 1998
	V	
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)

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